PTO/SB/21 (08-00)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application Number	09/783,023
		Filing Date	February 15, 2001
		First Named Inventor	Yoshiaki Fukuzumi et al.
		Group Art Unit	2823
		Examiner Name	G. Fourson, III
otal Number of Pages in This Submission	3	Attorney Docket Number	001701.00059
	ENCL	OSURES (check all that apply)	
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After Final	Petition		Proprietary Information
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Firm or Individual name Joseph M. Potenzi		EKTER	
Signature Leuy () Ju	lowth	२ 280(
Date June 26, 2002			
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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Yoshiaki Fukuzumi et al.

Serial No.: 09/783,023

Serial No.: 09/783,023

Examiner: G. Fourson, III

Filed: February 15, 2001

For: SEMICONDUCTOR DEVICE USING
FUSE/ANTI-FUSE SYSTEM AND
METHOD OF MANUFACTURING
THE SAME

Atty. Dkt. No. 01701.00059

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

This paper is responsive to the Restriction Requirement mailed June 4, 2002 and is filed prior to the expiration of the 30 day shortened statutory period set for response in the Restriction Requirement.

In response to the Restriction Requirement, Applicants elect without traverse the invention of Group II (i.e., claims 1-11) drawn to devices, classified in Class 257, subclass 330. Applicants note that the restriction requirement grouped claim 11 with Group I. However, because claim 11 is dependent on claim 1, Applicants believe that it should be examined with Group II.

Applicants reserve the right to file a divisional application directed to the nonelected invention prior to the termination of proceedings in the present application.

Yoshiaki Fukuzumi et al. - U.S. Serial No. 09/783,023

Please charge any fee associated with the filing of this paper to our Deposit Account No. 19-0733.

Respectfully submitted,

Ioseph M. Potenza

Registration No. 28,175

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Dated: June 26, 2002